

C 500 Family

	max. Clock Rate (MHz)	ROM (Byte)	ROM Protection	RAM (Byte)	I/O Lines (Inputs only)	ADC-Inputs/Resolution	Timer/Counter (16-Bit)	Interrupt vectors/Levels	Serial I/O	FCAN 2.oB active	USB 1.0 Interface	PWM	Mul./Div. Unit	Data Pointers (16-Bit)	Hardware Power Down	Watch-dog Timer	Osc Watch-dog	Packaging	
C501G-1E	24	8 K OTP	✓	256	32	-	3	6 / 2	USART	-	-	-	-	1	-	-	-	P-DIP-40 P-LCC-44 P-MQFP-44	Fully compatible with 80C52/C32 standard
C504-L/-2R C504-2E	24	- / 16 16 K OTP	✓	512	32	8 / 10	4	12 / 2	USART	-	-	6-ch	-	1	-	✓	✓	P-MQFP-44	CCU for DC motor control, Interrupt wakeable Power down
C513AO-L/-2R C513AO-2E	16	- / 16 16 K OTP	-	512	32	-	3	7 / 2	USART + SSC	-	-	-	-	1	-	✓	✓	P-LCC-44	Enhanced Power Saving Modes; Low Power; EMC Optimised
C508-4R C508-4E	20	32 K 32 K OTP	✓	1280	48	8 / 10	3	19 / 4	USART	-	-	6-ch	-	8	-	✓	✓	P-SDIP-64 P-MQFP-64	CCU for DC Motor Control, PLL (10 mHz ext. Clock), 2 Ports with 10 mA Sinking Current
C505L-4E	20 ¹⁾	32 K OTP	-	512	46	8 / 10	3	12 / 4	USART	-	-	4-ch	-	8	-	✓	✓	P-MQFP-80	LCD Driver On-Chip 128 segments; RTC with 32 kHz subclock
C505A-4E	20 ¹⁾	32 K OTP	✓	1280	34	8 / 10	3	12 / 4	USART	-	-	4-ch	-	8	-	✓	✓	P-MQFP-44	Enhanced power saving modes; Low EMI, Interrupt wakeable Power down
C505CA-4E/4R/2R	20 ¹⁾	32 K OTP/ROM 16 K ROM	✓	1280	34	8 / 10	3	12 / 4	USART	✓	-	4-ch	-	8	-	✓	✓	P-MQFP-44	Enhanced power saving modes; Low EMI, Interrupt wakeable Power down
C515-L	24	-	✓	256	56 (8)	Prog. REF 8 / 8	3	12 / 4	USART	-	-	4-ch	-	1	-	✓	✓	P-LCC-68	Compatible with SAB 80C515
C515C-L/-8R C515C-8E	10 ¹⁾	- / 64 K 64 K OTP	✓	2304	57 (8)	8 / 10	3	15 / 4	USART + SSC	✓	-	4-ch	-	8	✓	✓	✓	P-MQFP-80	Low Power, Low EMI
C517A-L	18	-	✓	2304	68 (12)	12 / 10	4	17 / 4	USART + UART	-	-	21-ch	✓	8	✓	✓	✓	P-LCC-84	Compatible with SAB 80C517A
C509-L	16 ¹⁾	-	-	3328	64 (15)	15 / 10	5	19 / 4	USART + UART	-	-	29-ch	✓	8	✓	✓	✓	P-MQFP-100	CMOS/TTL Ports Bootstrap-Loader
C541U-1E	12 ¹⁾	8 K OTP	✓	256	32 / 30	-	2	7 / 2	SSC	-	HUBless	-	-	1	-	✓	✓	P-LCC-44	USB-Transceiver-On-Chip;
SAB 80C535	20	-	-	256	56 (8)	Prog. REF 8 / 8	3	12 / 4	USART	-	-	4-ch	-	1	-	✓	-	P-LCC-68	Not for new designs.
SAB 80C515A	18	-	-	1280	56 (8)	8 / 10	3	12 / 4	USART	-	-	4-ch	-	1	✓	✓	✓	P-LCC-68	Not for new designs.
SAB 80C537	16	-	-	256	68 (12)	Prog. REF 12 / 8	4	14 / 4	USART + UART	-	-	21-ch	✓	8	-	✓	✓	P-LCC-84	Not for new designs.
SAB 80C517A	18	-	-	2304	68 (12)	12 / 10	4	17 / 4	USART + UART	-	-	21-ch	✓	8	✓	✓	✓	P-LCC-84	Not for new designs.

¹⁾ CPU-Clock